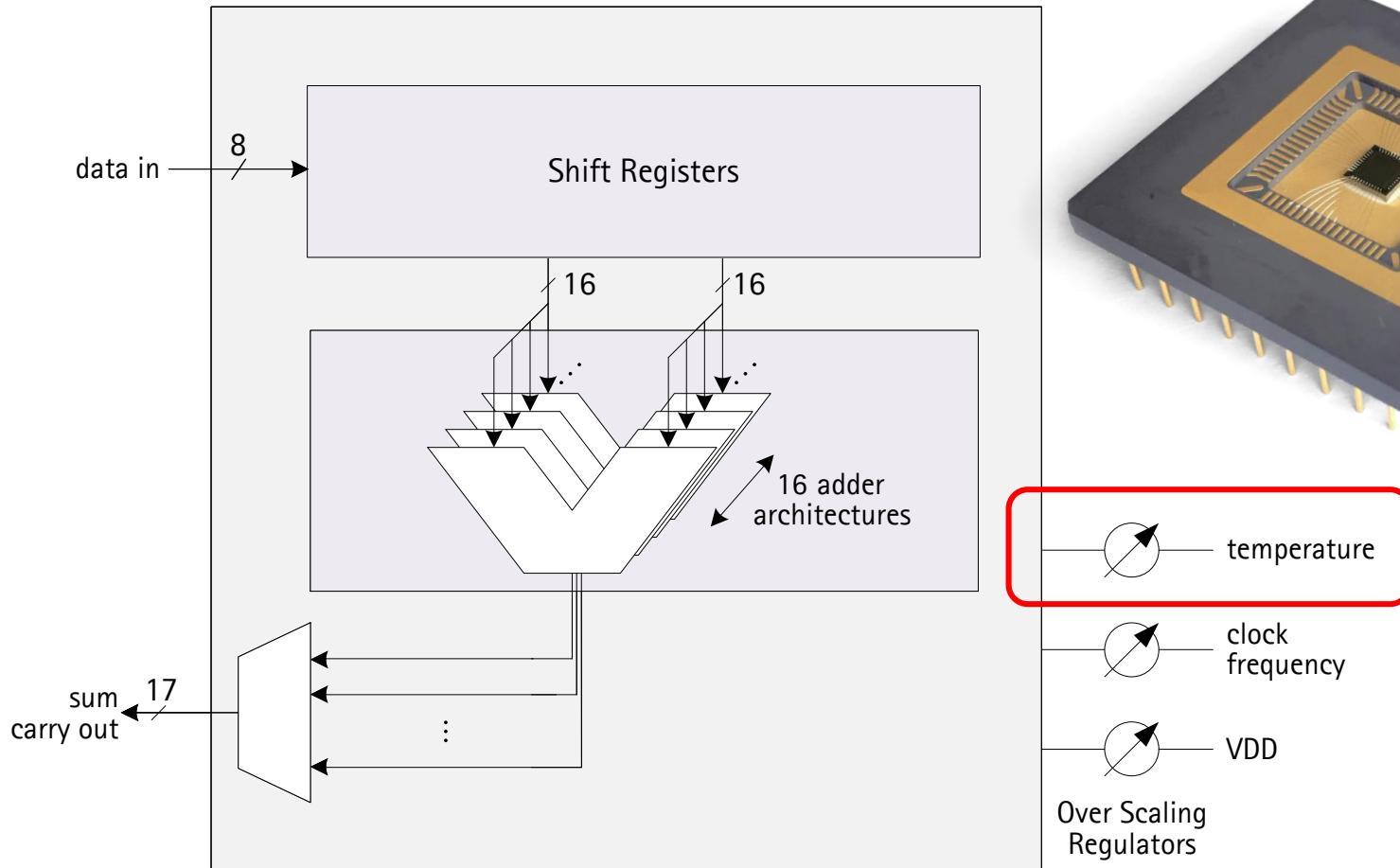


A Heated Testbench for High Temperature ICs

Application: Approximate Adder Architectures



A Heated Testbench for High Temperature ICs

